

CMOS OR NOR gate

It consists of a pair of N-MOS and a pair of P-MOS at input.

The N-MOS transistors are in parallel to put the output low when either input is high. The p-mos transistors are in series to put the output high when both the inputs are low as shown below.

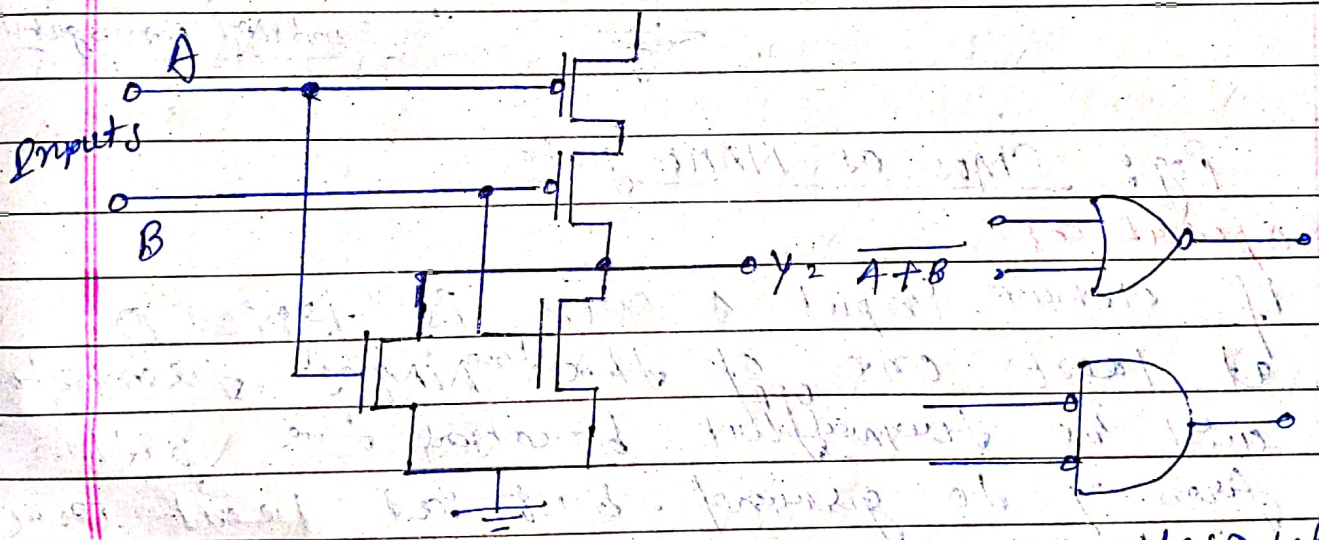


Fig: NOR gate symbol